

Applicants : Dan M. White
Serial No. : 10/748,427
Filed : December 30, 2003
Page : 9 of 12

Attorney Docket No.: INTEL-017PUS
Intel Docket No.: P17944

REMARKS

Claims 1 to 3, 5, 7, 9 to 13, 15 to 18, 20 and 24 to 32 are pending in this application.

Claims 1, 13 and 16 are the independent claims. Favorable reconsideration and further examination are respectfully requested.

Claims 1-3, 5, 7, 12, 13, 15 and 25-30 were rejected under 35 U.S.C. §103(a) as being obvious over Bade et al. (U.S. Patent Publication Number 2002/0059054, hereinafter “Bade”) in view of Mulchandani et al (U.S. Patent No. 5,701,488, hereinafter “Mulchandani”). Claim 9 was rejected under 35 U.S.C. § 103(a) as being obvious over Bade in view of Mulchandani and further in view of van Hoff et al (U.S. Patent Number 5,778,231, hereinafter “Hoff”). Claim 10 was rejected under 35 U.S.C. § 103(a) as being obvious over Bade, in view of Mulchandani, Hoff and further in view of Hall et al. (U.S. Patent No. 4,720,778, hereinafter “Hall”). Claim 11 was rejected under 35 U.S.C. § 103(a) as being obvious over Bade, in view of Mulchandani, and further in view of Smith et al. (U.S. Patent No. 6,311,324, hereinafter “Smith”). Claims 16-18, 20 and 31-32 were rejected under 35 U.S.C. 103(a) as being obvious over Bade, in view of Mulchandani, Hoff and further in view of Hall. Claim 24 is rejected under 35 U.S.C. 103(a) as being obvious over Bade et al., in view of Mulchandani, Hoff and Hall and further in view of Smith.

Claim 1 is directed to a method of displaying embedded firmware program information. The method includes displaying a first screen to interact with a user for high level function

selections, displaying a second screen to show hardware resources for a programmable circuit, displaying a third screen to show source code for a plurality of source code programs to control the programmable circuit and displaying a fourth screen to render symbolic information associated with the displayed source code. The symbolic information includes code labels, data labels referring to data structures including fields, data register names, and index register names; address locations for the code labels and the data labels; and listings associated with named registers, data labels for word, byte and short entities, and names of the data structures. The data structures and the fields of the data structures are individually expandable to show respective addresses and values of the word containing a start of the field.

The applied art is not understood to disclose or to suggest the foregoing features of claim 1. In particular, neither Bade nor Mulchandani whether taken separately or in combination discloses or suggests that the data structures and the fields of the data structures are individually expandable to show respective addresses and values of the word containing a start of the field.

Applicants further submit that Applicants' arguments in the previous Response dated September 5, 2008 were not all addressed by the Examiner as required by MPEP §707.07 (f) and respectfully request such action. For instance, Applicants note that the Examiner has merely repeated his previous office action (see, for example, see page 5 of the Office Action) and has ignored Applicants' arguments in the Response to Applicants' arguments (see pages 16 to 18 of the Office Action).

The Examiner has still not indicated where Bade teaches values of the word containing a start of the field nor has the Examiner specifically shown where this limitation may be found in the cited art. Rather, the Examiner seems more focused on "expandable" than "expandable to

show values of the word containing a start of a field" and therefore, does not explicitly show to Applicants where in Bade the relevant portions related to values of the word containing a start of the field. Applicants further note that one of ordinary skill in the art would not be able to read the Examiner's rejection and the Bade reference and determine where Bade teaches values of the word containing a start of the field. Therefore, Applicants respectfully request that the Examiner address Applicants' concerns.

As stated in the previous office action, Mulchandani does not disclose or suggest that the fields of the data structures are individually expandable much less teach that the fields of the data structures are individually expandable to show respective addresses and values of the word containing a start of the field nor has the Examiner made such an assertion in the office Action. Since neither reference teaches values of the word containing a start of the field, the suggested combination would not teach the recited limitation either.

Therefore, Applicants respectfully submit that the Examiner has not established a *prima facie* rejection.

Claims 13 and 16 include the corresponding feature that the data structures and the fields of the data structures are individually expandable to show respective addresses and values of the word containing a start of the field as recited in claim 1. Applicant submits that the cited art should also be withdrawn with respect to claims 13 and 18 for at least the same reasons as claim 1.

For at least the foregoing reasons, Applicants request withdrawal of the art rejection.

Applicants submit that all dependent claims now depend on allowable independent claims.

Applicants : Dan M. White
Serial No. : 10/748,427
Filed : December 30, 2003
Page : 12 of 12

Attorney Docket No.: INTEL-017PUS
Intel Docket No.: P17944

It is believed that all of the pending claims have been addressed. However, the absence of a reply to a specific rejection, issue or comment does not signify agreement with or concession of that rejection, issue or comment. In addition, because the arguments made above may not be exhaustive, there may be reasons for withdrawing the prior art cited with regards to any or all pending claims (or other claims) that have not been expressed. Finally, nothing in this paper should be construed as intent to concede any issue with regard to any claim, except as specifically stated in this paper, and the amendment of any claim does not necessarily signify concession of unpatentability of the claim prior to its amendment.

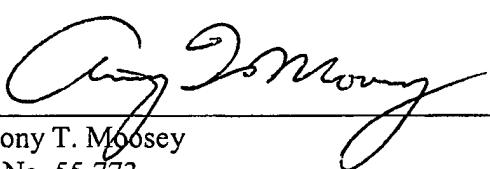
Applicants submit that the entire application is now in condition for allowance. Such action is respectfully requested at the Examiner's earliest convenience.

Applicants' attorney can be reached by telephone at (781) 401-9988 ext. 123.

No fee is believed to be due for this Response; however, if any fees are due, please apply such fees to Deposit Account No. 50-0845 referencing Attorney Docket: INTEL-017PUS.

Respectfully submitted,

Date: 6 February 2009


Anthony T. Moosey
Reg. No. 55,773

Attorneys for Intel Corporation
Daly, Crowley, Mofford & Durkee, LLP
354A Turnpike Street - Suite 301A
Canton, MA 02021-2714
Telephone: (781) 401-9988 ext. 123
Facsimile: (781) 401-9966
106644